

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 10-28-2003
Application Type:: Regular
Subject Matter:: Utility
Title:: Counter-Based Phased Clock
Generator Circuits and Methods
Attorney Docket Number:: X-1464-1P-1P US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 15
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Andy
Middle Name:: T.
Family Name:: Nguyen
Street:: 775 Salt Lake Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95133

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

R presentative Customer Number::	24309	
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DOMESTIC PRIORITY INFORMATION

<u>Application::</u>	<u>Continuity Type::</u>	<u>Parent Application::</u>	<u>Parent Filing Date::</u>
This Applic.	CIP	10/651,811	08-29-03
10/651,811	CIP	10/618,329	07-11-03

ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124